



# Jeong-seok LEE

## YEAR OF PRACTICE

- 2021 ~ Present

## ADMISSIONS / AFFILIATIONS

- Admitted to Patent Bar
- KPAA (Korean Patent Attorneys Association)

## CONTACT

- ☎ + 82-2-6714-1170
- ✉ jsLee@nampat.co.kr
- 🌐 Korean / English / Japanese

## EDUCATIONAL BACKGROUND

- Yonsei University, MS in Electrical and Electronic Engineering (2013)
- Chung-Ang University, BS in Electrical and Electronic Engineering (2002)

## TECHNICAL EXPERTISE

- Optical Elements, Semiconductor Devices, Wireless Communication Networks, Data Processing System and Method, and Electric Digital Data Processing

## PROFESSIONAL EXPERIENCE

- Handling domestic/international PCT applications and office actions for the patent and utility model
- Handling patent invalidation trials, and oppositions regarding semiconductor memory
- FTO reports and design-arounds regarding the wireless communication networks
- Advising patent technology valuation for the semiconductor and circuit system
- Analysis of patent trend in semiconductor manufacturing processes and establishing IP strategy

## OTHERS

- Exchange work at Blakely Sokoloff Taylor Zafman LLP, the US law firm
- "How to design around a patent to avoid infringement" Contribution to Display Asia
- Lecturer of Korea Productivity Center